

Form PTO-1249

ATTY. DOCKET NO.
1248-0509P

APPLICATION NO.
09/615,956

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

APPLICANT
Kumi MIYACHI et al

FILING DATE
July 13, 2000

GROUP	2133
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS

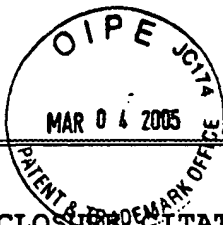
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Form PTO-1449

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2133**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	Kind	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	US						
	US						
	US						
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	US						

FOREIGN PATENT DOCUMENTS

Office	DOCUMENT NUMBER	Kind	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
JP	06-066884		1994-03-11	JAPAN			Abs	

OTHER DOCUMENTS

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	SAKAMAKI, Kazumi, Basic And Application of JTAG Test, CQ Publication, 12/1/98, p. 37.

EXAMINER

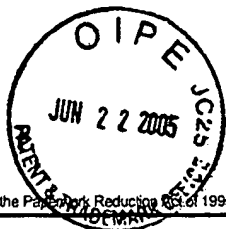
Esaw Abraham

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PTO/SB/08a/b (08-03)
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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	09/615,956-Conf. #003629
				Filing Date	July 13, 2000
				First Named Inventor	Kumi MIYACHI
				Art Unit	2133
				Examiner Name	E. T. Abraham
Sheet	1	of	1	Attorney Docket Number	1248-0509P

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

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NON PATENT LITERATURE DOCUMENTS			
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	CA**	Satish, Keshava Iyengar, Tutorial on Design For Testability (DFT), "An ASIC Design Philosophy for testability from Chips to Systems," ASIC CONFERENCE AND EXHIBIT, PROCEEDINGS., SIXTH ANNUAL IEEE INTERNATIONAL, ROCHESTER, NY, USA, 27 September 1993, pages 130-139	

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IDS Not
Considered

Examiner Signature	E. T. Abraham	Date Considered	02/14/06
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Birch, Stewart, Kolasch & Birch, LLP

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